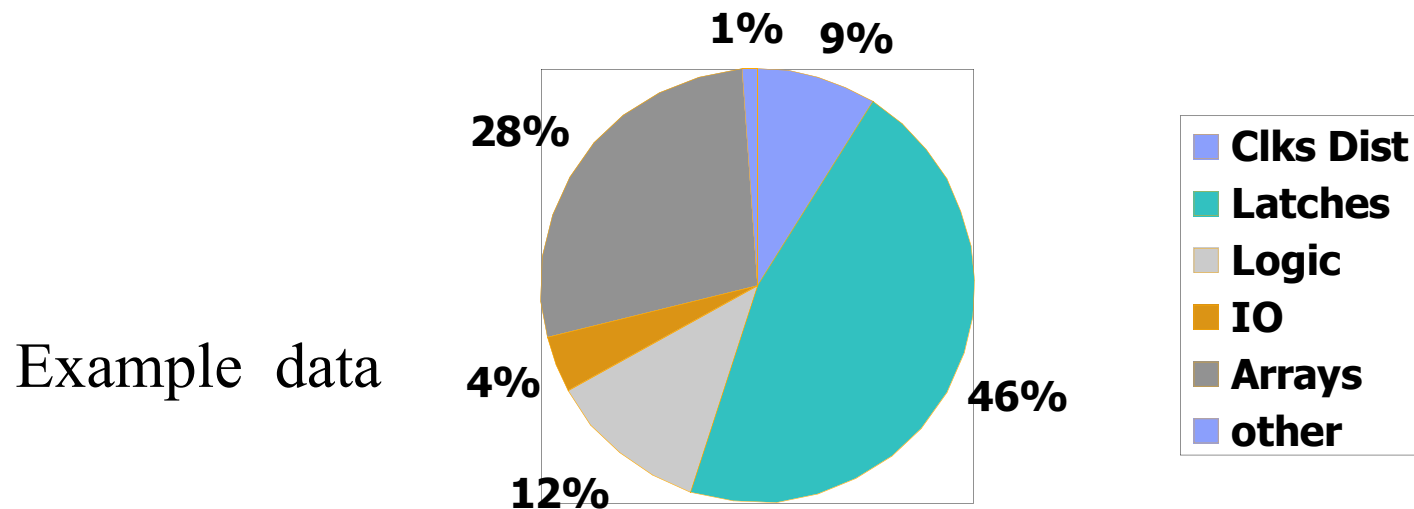# Processor Power Pie-Chart: Another View

- **High performance processors (prior/current generation) typically burn most of their power in the clocked latches and arrays (registers, caches).**

(taken from: Bose, Martonosi, Brooks: Sigmetrics-2001 Tutorial)

Example data

■	**Clks Dist**
■	**Latches**
■	**Logic**
■	**IO**
■	**Arrays**
■	**other**

1% 9% 28% 4% 46% 12%

Pre-silicon ckt-sim based; assumes: no clock-gating