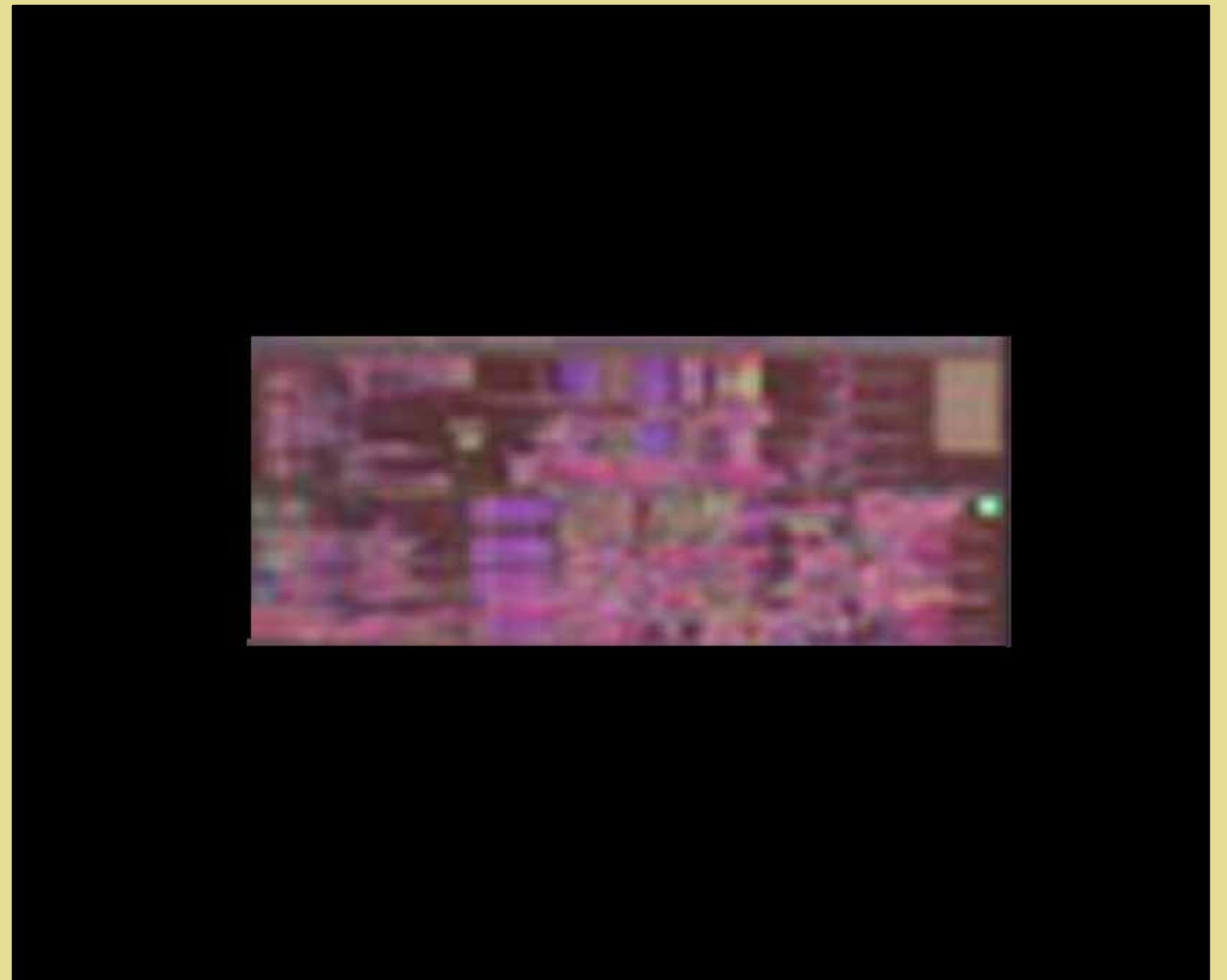


Silverthorne and Penryn

Penryn 6M

Silverthorne

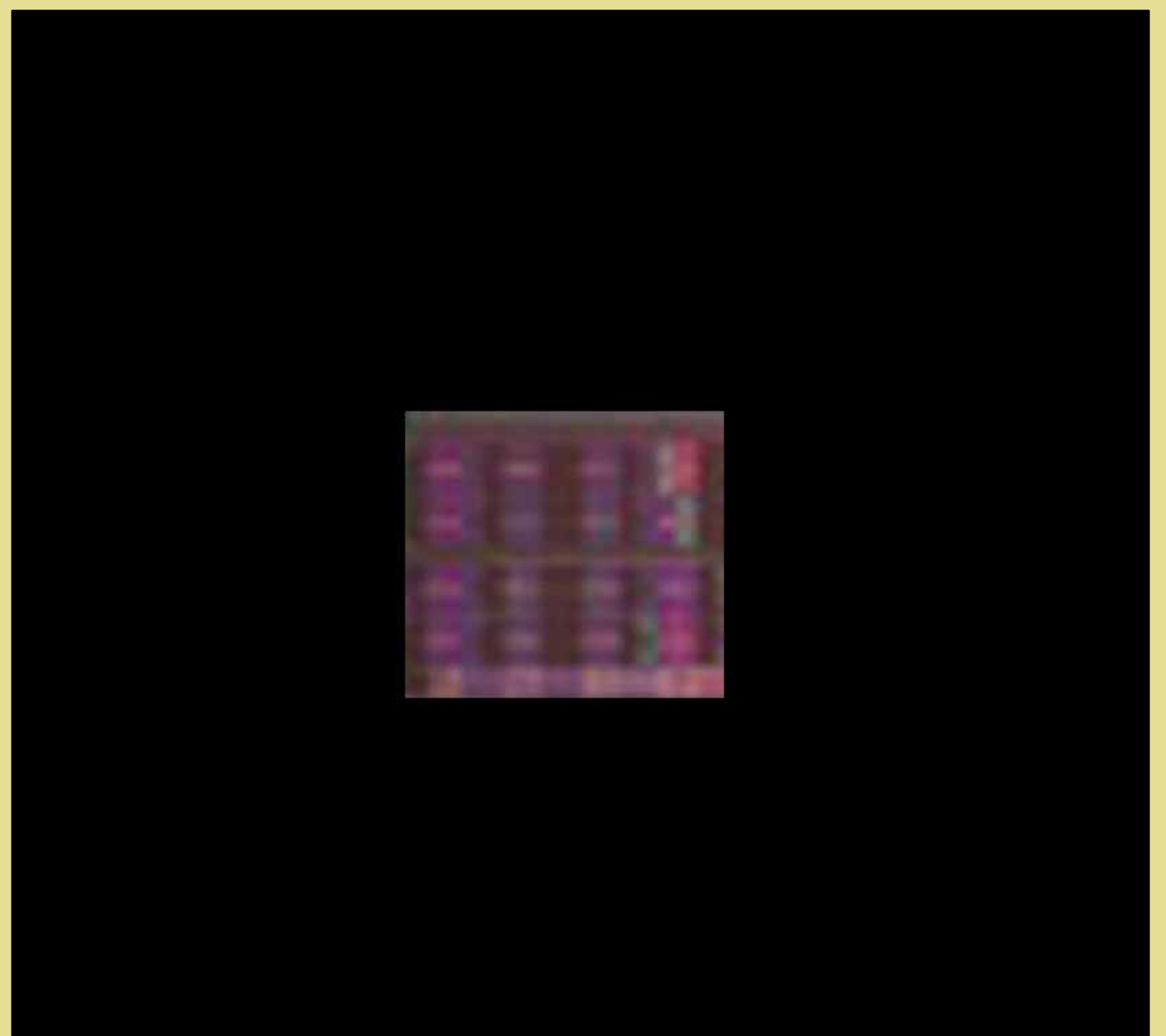
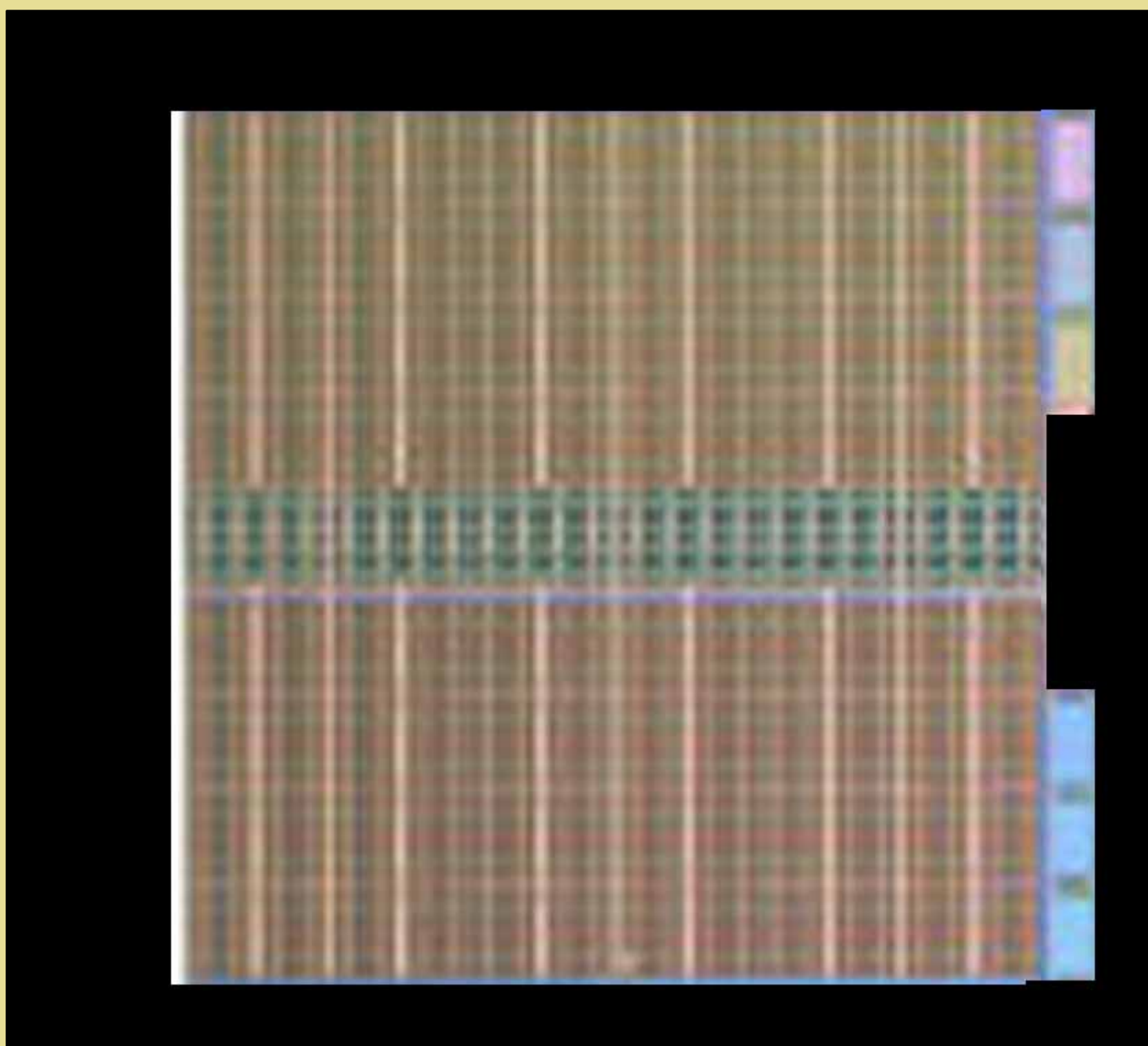
Single CPU Core



CPU Core >19M Transistors

CPU Core 10M Transistors?

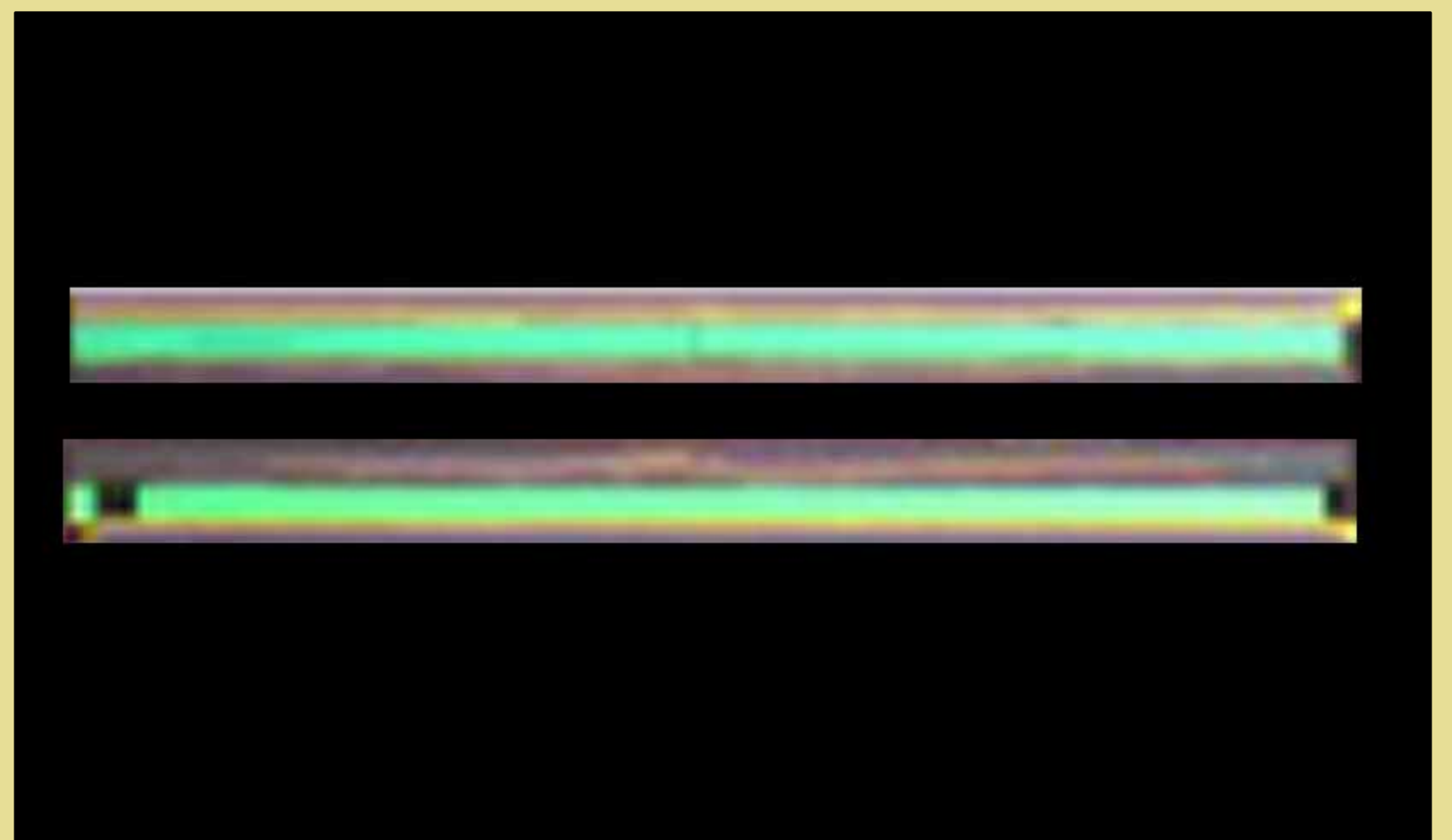
L2 Cache



6MB L2 Cache

512KB L2 Cache?

I/O(FSB) Pads



I/O Address and I/O Data

I/O Address and I/O Data

