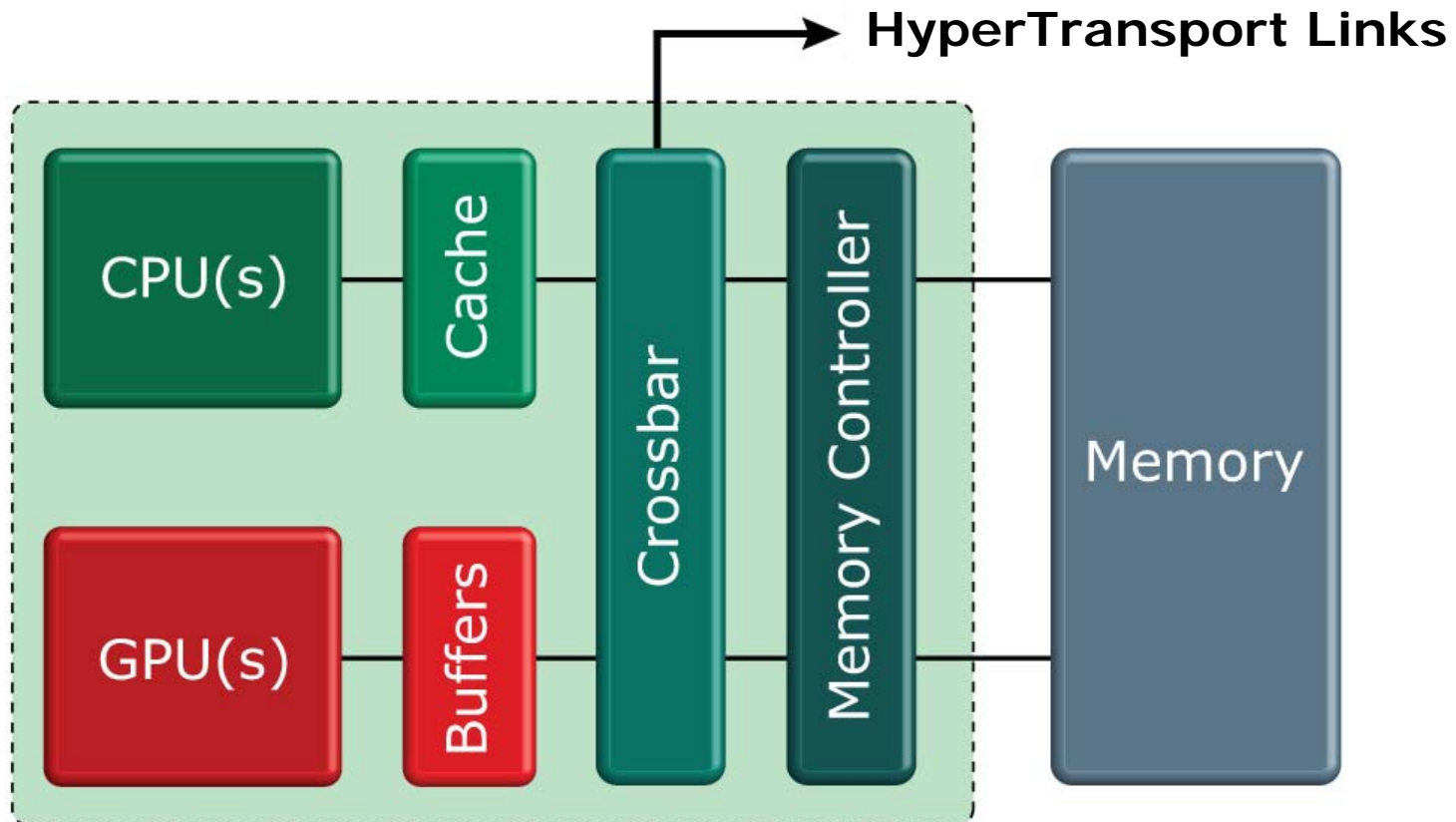


The Efficiency Benefits of Silicon-Level Integration



Planned step-function improvement in power/performance with cost reduction opportunities