Trends in DRAM bandwidth
Improved Efficiency is the Answer

Higher per-socket bandwidth demands

Diverse streams increase conflicts

DRAM efficiency declining
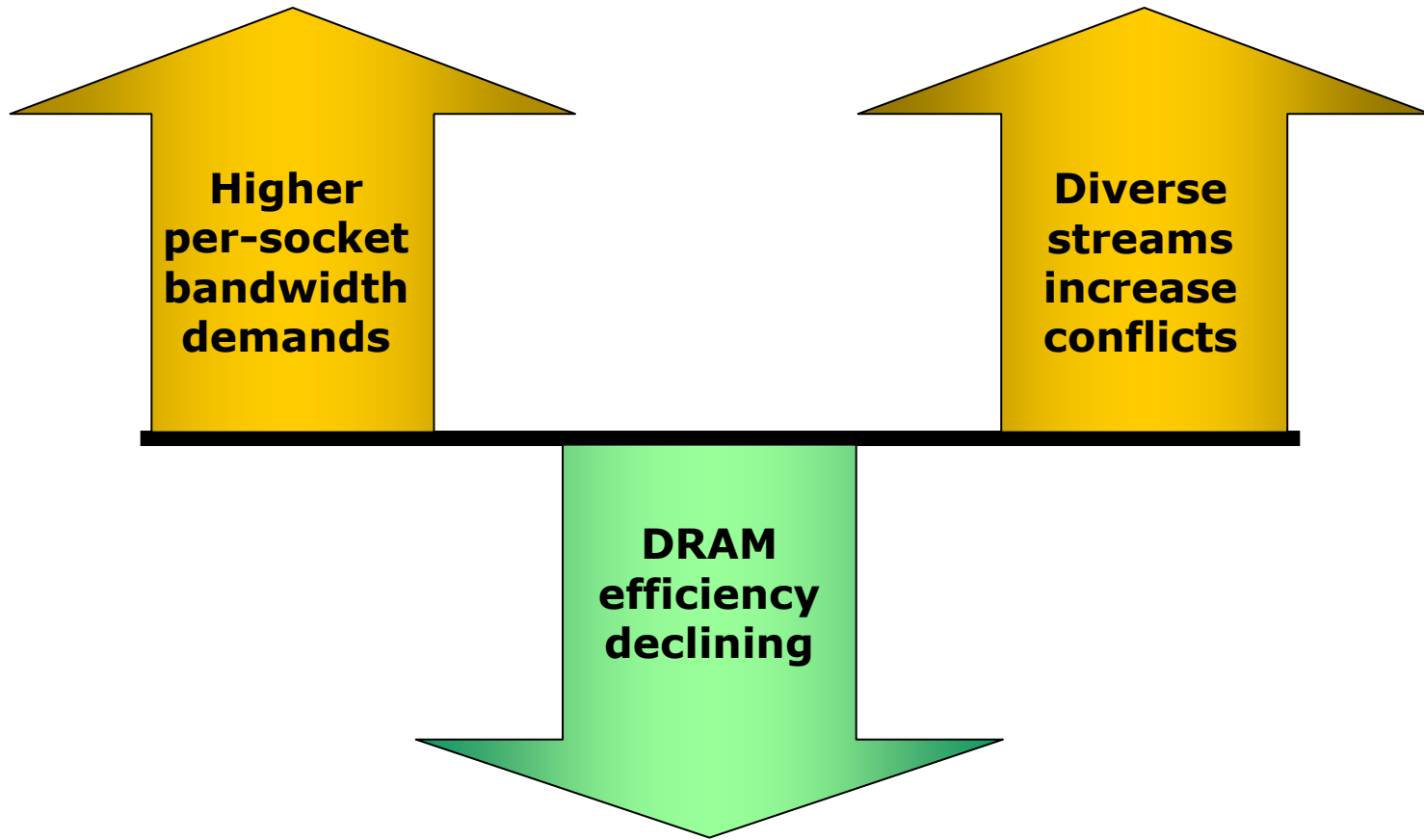
We must improve ***delivered*** DRAM bandwidth

AMD