

- **Threaded architectures**
- **10GHz operation**
- **Chip level multiprocessing**
- **Huge scale MP machines**
- **Much higher performance superscalar, out of order CPU core**
- **Media/vector processing extensions**
- **Security and virtualization**
- **Huge caches**
- **Static and dynamic Power management**
- **Branch and memory hints**
- **GHz performance IO**

All are essentially instruction set agnostic.