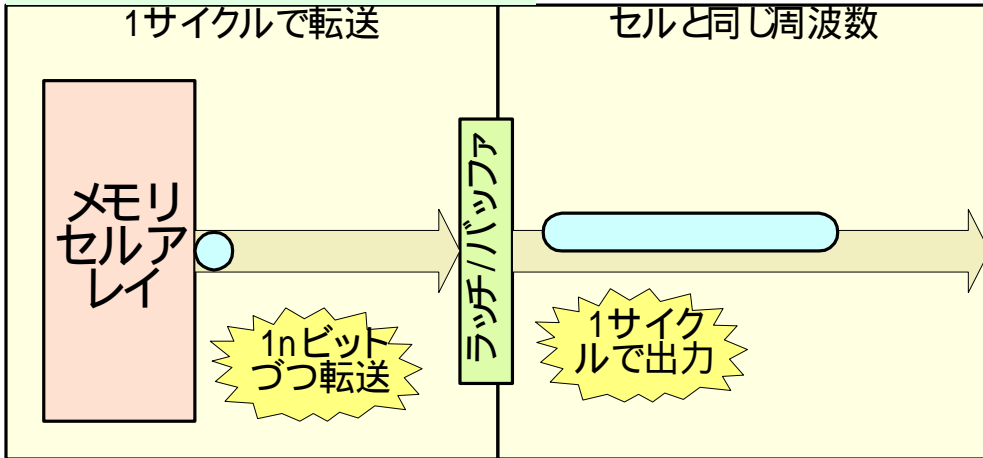
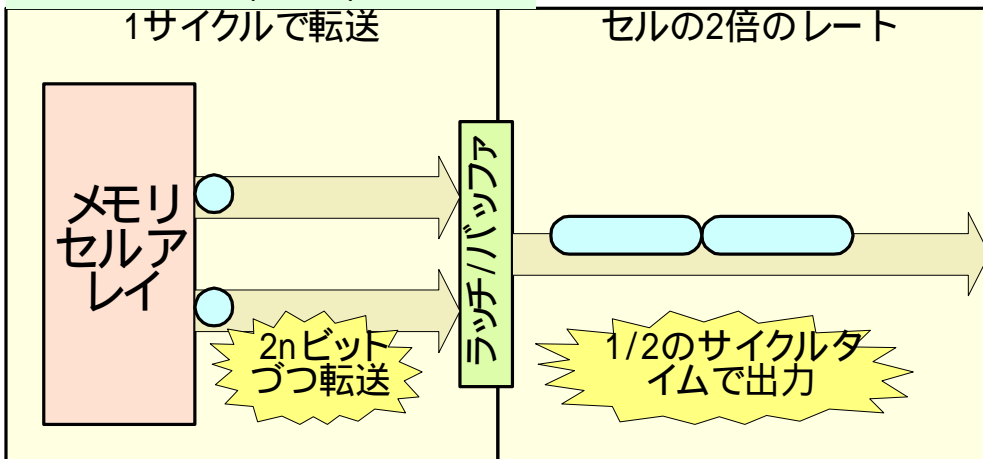


DDRのPrefetchアーキテクチャ概念図

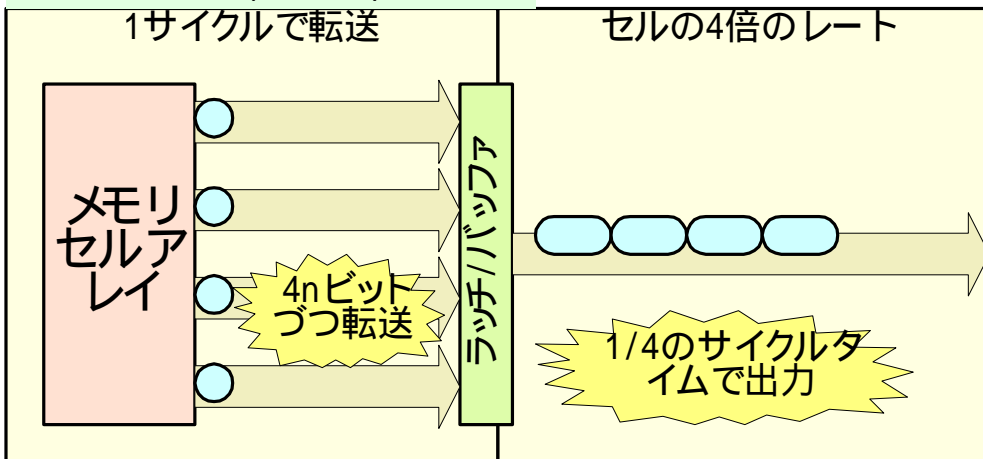
Prefetch 1 (SDRAM)



Prefetch 2 (DDR)



Prefetch 4 (DDR2)



Prefetch 8 (DDR3)

